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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Bruce Harmon et al.  
Assignee: Synopsys, Inc.  
Title: METHOD AND APPARATUS FOR MODELING USING A  
HARDWARE-SOFTWARE CO-VERIFICATION ENVIRONMENT  
Serial No.: 09/637,984 File Date: August 11, 2000  
Examiner: Crystal J. Barnes Art Unit: 2121  
Docket No.: SYN-0156

Date: May 21, 2004

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Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Technology Center 2100

AMENDMENT IN RESPONSE TO THE FIRST OFFICE ACTION

Initial Comments

Claims 1-29 are pending in the present application.

Figure 2 is objected to because of an incorrect reference number. The Specification is objected to because of an informality. The Abstract is objected to because of a typographical error. Claims 22 and 23 are potentially objected to because of Claims 11 and 17 being substantial duplicates.

Claims 11, 22, and 28 are rejected under 35 U.S.C. 112 as being indefinite. Claims 1-5 and 8-10 are rejected under 35 U.S.C. 103(a) as being obvious over U.S. Patent (Klein) in view of logical reasoning. Claims 11-14, 17-19, 22, and 23 are rejected under 35 U.S.C. 102(b) as being anticipated by Klein. Claims 6, 7, 15, and 16 are objected to as being dependent upon

a rejected base claim, but would be allowable is rewritten in independent form.